

विद्युत अभियांत्रिकी विभाग / DEPARTMENT OF ELECTRICAL ENGINEERING
राष्ट्रीय प्रौद्योगिकी संस्थान श्रीनगर / NATIONAL INSTITUTE OF TECHNOLOGY SRINAGAR
(शिक्षा मंत्रालय, भारत सरकार के तत्वावधान में राष्ट्रीय महत्व का एक संस्थान)
(An Institute of National Importance under the aegis of Ministry of Education, Govt. of India)
हजरतबल, श्रीनगर, जम्मू-कश्मीर, १९०००६, भारत
Hazratbal, Srinagar Jammu and Kashmir, 190006, INDIA



Advertisement for the post of Junior Research Assistant under JKST&IC Project

Applications are invited for one post of Junior Research Assistant (JRA) for the sanctioned research project (No.: JKST&IC/SRE/231-35 dt. 23-01-2024) of JKST&IC entitled "Performance Evaluation of Solar PV-thermal Technology for Efficient Electrical and Thermal Applications for Inhabitants of Kashmir Valley". The post is purely temporary and co-terminus with the project. The duration of the JRA appointment is initially for 8 months and may be extended further.

Details	
Position Title	Junior Research Assistant (JRA)
No. of Post	01 (One)
Essential Qualifications	B.Tech and M.Tech in Electrical Engineering / Energy Systems or equivalent with minimum 60% aggregate marks (CGPA \geq 6.5/10) to qualify under Open/OBC category and minimum 55% aggregate marks (CGPA \geq 6.0) for candidates under SC/ST category. OR B.Tech (4 years) in Electrical Engineering / Energy Systems or equivalent with a minimum 75% aggregate marks (CGPA \geq 8.0/10) to qualify under Open/OBC category and minimum 70% aggregate marks (CGPA \geq 7.5) for candidates under SC/ST category, and must have qualified GATE with valid score.
Desirable	Candidates having sound knowledge of solar photovoltaic-thermal (PVT) technology with a good understanding of practical and simulation design of solar PV systems.
Age	The upper age limit is 25 years. Age relaxation for reserve candidates will be given as per Govt. norms.
Tenure of Appointment	The initial appointment will be for eight (8) months which may be extended further.
Emoluments	₹20,000/-per month
How to Apply?	The filled application (via Google form) attaching the CV and necessary testimonials
Submission	Google Form https://forms.gle/zk4GD7iG66kopkks8
Last Date	15th May 2024
Whom to Contact?	Dr. Asadur Rahman Principal Investigator (PI), Department of Electrical Engineering, NIT Srinagar-190006 (J&K) Email: asadur@nitsri.ac.in Contact No. +91 9957497127 (M)

Note:

- CV should include contact details (address, mobile phone no., email ID), date of birth, qualifications, research experience, and publication details (if any).
- All the testimonials (scanned copies of mark sheets, degree certificates, experience certificates, GATE certificates, valid identity proof, etc.) in pdf as a single compressed (zip/RAR format) file.
- No TA/DA will be paid for attending the interview.

Deadline for application: 15th May 2024.

Screening & Interview Process

Candidates will be shortlisted based on their merit and as per the requirements of the project. Mere qualification of eligibility criteria does not guarantee shortlisting. The date, time, and venue of the interview will be intimated to short-listed candidates via email. Candidates who are already employed should produce a 'No Objection Certificate' (NOC) from the present employer.

For any further clarifications, candidates may contact:

Dr. Asadur Rahman

Principal Investigator (PI),

Department of Electrical Engineering, NIT Srinagar-190006 (J&K)

Email: asadur@nitsri.ac.in

Contact No. +91 9957497127 (M)