

Department of Electronics & Communication Engineering
National Institute of Technology, Srinagar

Notice

Dated: 02-07-2021

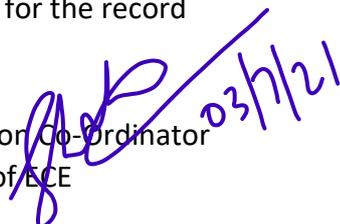
Lab Exam Schedule for BTech (4th Semester)

Lab	Time	Date	Faculty
Electronics-II Lab (ECE)	10 AM	05-07-2021	Prof. A. G. Mir
Digital Electronics & Logic Design (IT)	10 AM	05-07-2021	Mrs. Uferah Maqbool
Basic Electronics Engineering Lab (CHE)	10 AM	05-07-2021	Dr. Sayeed Ahamad
Digital Electronics Lab (ECE)	10 AM	06-07-2021	Dr. Ashok Kumar
Communication System – Lab (ECE)	10 AM	07-07-2021	Dr. Shahid M Shah
Electronics Lab II (ELE)	10 AM	09-07-2021	Dr. Brajendra Singh

Copy to:

- 1) Concerned Faculty
- 2) I/C Examination CSE, CHE, ELE & IT
- 3) Office for the record

I/C Examination Co-Ordinator
Department of ECE


03/7/21

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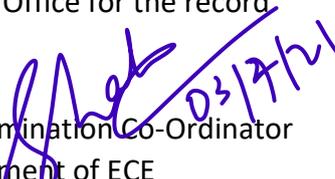
Dated: 02-07-2021

Lab Exam Schedule for BTech (6th Semester)

Lab	Time	Date/Time	Faculty
Electronic Design & Automation Tools	10:00 AM	06-07-2021	Dr. S. Aamir Ahsan

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Department of ECE

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Lab Exam Schedule for MTech (CIT & ME)

Lab	Time	Date	Faculty
Advanced Design Techniques Lab (CIT & ME)	10:00 AM	05-07-2021	Dr. Burhan Khurshid
Laboratory II (Image Processing) (CIT)	10:00 AM	07-07-2021	Dr. Fareeda Khursid

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Department of ECE

