

Short Term Course
On
Communication System Design

(Under TEQIP-III)

July 1-5, 2019

Organized by:

**Department of Electronics and
Communication Engineering**

Course Coordinators:

Dr Shahid Mehraj Shah
Dr. Omkar Singh



**Department of Electronics and Communication
Engineering**

National Institute of Technology Srinagar
190006, J&K

Website: www.nitsri.ac.in

About the Institute

National Institute of Technology (NIT), Srinagar was established in the year 1960 as Regional Engineering College (REC) by Government of India during the first Five Year Plan (1956–61). It was given the status of National Institute of Technology in the year 2002, and functions under the aegis of Ministry of Human Resource development (MHRD), Government of India. As an Institute of National Importance, it imparts high quality technical education in Engineering, Technology and Science to produce competent technical manpower for the country.

Location

The institute is located on the western bank of the world heritage site, Dal Lake near the Hazratbal Shrine in the north eastern region of the Srinagar city. The institute is located 23 km from the Srinagar International Airport and 13 km from Srinagar railway station.

About the Department

The Department of Electronics and Communication Engineering was established in 1984 and offers B.Tech in Electronics and Communication Engineering, M.Tech in Microelectronics (ME) and M.Tech in Information and Communication Technology (ICT). The Department also offers the Doctoral programmes in the field of Biometrics, Communication, Microelectronics, Signal/Image Processing, Machine learning, network security and VLSI Design. The department has established state-of-art laboratories with sophisticated equipments for undergraduate, post graduate and PhD research work. The department is equipped with contemporary laboratory equipments required for the excellent growth of

students. The department has state of art labs including VLSI Lab, Optical fiber communication Lab, Image Processing Lab, Information and Network Security Lab, Biometric Lab and machine learning lab.

Objective of Course

The revolution of communication systems has tremendously changed the daily lives. Cellular systems, WiFi, Internet, Radio & TV broadcasting and now Internet of Things (IoT) are some of the key technologies which enabled this change. The focus of this STC will be the basic design aspects of communication systems. This course will also have lectures on the modern communication system design, wherein the topics ranging from MIMO, Physical Layer Security, Antenna design for 5G systems will be covered. This course will also have sessions on next generation communication system design, like Intelligent Reflecting Surfaces, proposed for 6G communication systems. There will be hands on session on USRP device, MATLAB, ADS Simulator for RF design, CST Simulator for Antenna design.

Resource Persons

The resource persons for the course will be mostly from IIT's, NIT's, and other reputed education and research organizations. Tentatively following speakers will be delivering the lectures

- 1) Prof. S. K. Koul (IIT Delhi)
- 2) Prof. M. Zafar Ali Khan (IIT Hyd.)
- 3) Dr. Ajay Singh (IIT Jammu)
- 4) Dr. Javed GS (Terminus Circuits)

Organizing Committee

Patron

Professor (Dr.) Rakesh Sehgal

Director

National Institute of Technology, Srinagar

Co-Patron

Professor (Dr.) M. F. Wani

Co-ordinator TEQIP-III and Professor,
National Institute of Technology, Srinagar

Dr. Farida Khurshid

HOD Department of ECE,
National Institute of Technology, Srinagar

Convener

Dr G. R. Begh

Associate Professor,
Department of ECE, NIT Srinagar

Coordinators

Dr Shahid Mehraj Shah

Assistant Professor
Email: shahidshah@nitsri.net
Mobile: 7889519497

Dr Omkar Singh

Assistant Professor
Email: omkar.parihar@nitsri.net
Mobile: 9419279382

Organizing Team members

Prof G M Rather

Prof Najeeb-U-Din

Er A A Mir

Dr Gausia Qazi

Er A G Mir

Dr G R Begh

Dr S A Ehsan

Dr Amandeep Singh

Registration Details

Registration Fee (per participant)

Students/ Research Scholars	Rs. 1500/-
Faculty Members/ Scientists	Rs. 2500/-
Industry Persons	Rs. 3000/-

Account details for payment of Fee:

A/c Name: TEQIP-III

A/c No: 0391040100011025

Bank Name: J & K Bank

Bank Branch: REC Srinagar

IFSC Code: JAKA0RECSGR (0=zero)

Important Dates

Reporting Date and Time at the Institute:

Date: 01/07/2019 at 9.00 AM.

Venue: NIT Srinagar

The complete registration form should reach by speed post or email latest by June, 28, 2019 at the following address:

Corresponding Address:

Dr Shahid Mehraj Shah /Dr. Omkar Singh

Course Coordinators

Department of Electronics and Communication Engineering, National Institute of Technology Srinagar, Hazratbal Kashmir-190006

Mobile: 9419279382; 7889519497

E-mail: shahidshah@nitsri.net,

omkar.parihar@nitsri.net

After registration kindly fill the registration form online also on this STC website:

<https://sites.google.com/view/stc2019comm-design/>



**National Institute of Technology Srinagar
Hazratbal (Kashmir)-190006**

Short Term Course

On

Communication System Design

(Under TEQIP-III)

July 1-5, 2019

Registration Form

Name:

Designation:

Department:

Organization:

Address:

.....

Tel/Fax:

Email:

Accommodation: Required / Not required

Online Reference No.....

Bank Name:

Amount in Rs.....

Dated:

Signature with Date:

Sponsorship Certificate

The applicant is hereby sponsored and will be permitted to attend the short term course "Communication System Design", if selected.

Date:

Signature and Seal of
Sponsoring authority