



Corrigendum Notice

DATED: 01-10-2024

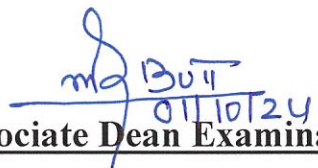
This is with regard to the Date Sheets issued under No: *NIT/ADE/2024/3506*; Dated: **01-10-2024** for **B. Tech 3rd Semester (New Scheme) Mid Term Examination Session Autumn-2024**. The date for Mid-Term Examination in respect of **B. Tech 3rd Semester (New Scheme)** in the below mentioned subjects have been rescheduled as under: -

Branch	SUBJECT	DATE & TIME
ELECTRICAL ENGG.	Electronic Devices and Circuits	ON 15-10-2024 (TUESDAY) at 11:15 AM TO 12:30 PM INSTEAD OF 16-10-2024 (WEDNESDAY) at 11:15 AM TO 12:30 PM.
ELECTRICAL ENGG.	Signals & Systems	ON 16-10-2024 (WEDNESDAY) at 11:15 AM TO 12:30 PM INSTEAD OF 15-10-2024 (TUESDAY) at 11:15 AM TO 12:30 PM.

Note: The Venue however will remain the same.

No: NITS/ADE/2024/3512

Dated: 01-10-2024


Associate Dean Examinations

Copy to: -

1. Dean Academic Affairs for information.
2. Concerned Heads of the Departments/Sections for information and necessary action from their end.
3. Officer In-Charge website with a request to kindly upload the notice on the Institute website.
4. Officer I/C Classrooms.
5. SS to Director for kind information of the Director.
6. All Notice Boards.
7. Concerned file.